

L Number	Hits	Search Text	DB	Time stamp
-	2	"20030014578".pn.	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:11
-	13	smt near10 multiplex\$	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:22
-	31	smt near10 (double adj side\$1)	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:29
-	366	(smt "surface mount") near10 ((double two both) adj side\$1)	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:53
-	41	multiplex\$ & ((smt "surface mount") near10 ((double two both) adj side\$1))	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:36
-	40	(via vias) & multiplex\$ & ((smt "surface mount") near10 ((double two both) adj side\$1))	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:36
-	7	(smt "surface mount") near10 ((double two both) adj side\$1) near10 (via vias)	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:37
-	14549	"memory module"	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:53
-	12	((smt "surface mount") near10 ((double two both) adj side\$1)) & "memory module"	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:53
-	2	(smt "surface mount") & ("memory module" NEAR10 multiplex\$)	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:53
-	315	"memory module" NEAR10 multiplex\$	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:54
-	626	365/230.02.ccls.	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:56
-	36	"memory module" & 365/230.02.ccls.	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 08:56
-	13888	"integrated circuit" & package & pins	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:14

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-	106367	memory near5 controller\$1	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:14
-	47	"integrated circuit" & package near5 (memory near5 controller\$1) & pins	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:19
-	15	bilateral\$ near3 symmetric\$ near5 pins	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:20
-	1	multiplex\$ & (bilateral\$ near3 symmetric\$ near5 pins)	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:20
-	3	multiplex\$ near5 pins near10 symmetric\$	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:21
-	2	multiplex\$ near5 pins near10 bilateral\$	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:22
-	173	circuit\$ near5 package\$ near10 multiplex\$	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:22
-	29	symmetric\$ & (circuit\$ near5 package\$ near10 multiplex\$)	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:23
-	489	"circuit package" & memory & pads & traces	USPRT; US-PCFUB; EPO; JPO; DERMENT; IBM TDB	2003/12/18 13:59

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